

# Contents

<b>1</b>	<b>Introduction .....</b>	<b>1</b>
<b>2</b>	<b>Scope .....</b>	<b>1</b>
<b>3</b>	<b>Abbreviations and terms.....</b>	<b>2</b>
<b>4</b>	<b>Requirements from the system perspective.....</b>	<b>2</b>
<b>5</b>	<b>Grid forming relevant design parameters of HVDC systems and DC-connected PPMs .....</b>	<b>3</b>
<b>6</b>	<b>Application-specific guidelines .....</b>	<b>4</b>
6.1	General .....	4
6.2	HVDC systems .....	5
6.3	DC-connected PPMs .....	5
<b>7</b>	<b>Test network, verification method and reference behaviour for HVDC systems and DC-connected PPMs .....</b>	<b>5</b>
7.1	Test networks .....	5
7.2	Definition of test scenarios.....	8
7.2.1	Phase angle step in network voltage .....	9
7.2.2	Phase angle step followed by linear frequency change .....	9
7.2.3	Voltage magnitude step in the network.....	10
7.2.4	Presence of a negative-sequence component in the network .....	10
7.2.5	Presence of harmonics in the network.....	11
7.2.6	Presence of subharmonics in the network.....	11
7.2.7	Change in the network impedance .....	12
7.2.8	Islanding with DUT and active load .....	12
7.2.9	Islanding with 2 DUT and active load .....	13
7.2.10	Change in the network impedance with 2 parallel DUT .....	13
7.3	Reference behaviour .....	14
7.3.1	Procedure .....	14
7.3.2	DC-connected PPMs and HVDC systems with options of energy compensation from independent energy sources .....	15
<b>8</b>	<b>Summary and prospect.....</b>	<b>26</b>
<b>9</b>	<b>Bibliography .....</b>	<b>27</b>
<b>A.</b>	<b>Annex .....</b>	<b>28</b>
A.I.	Verification procedure – Application-specific guidelines.....	28
A.I.A.	Required DUT behaviour for evaluation of grid forming control behaviour (high-level control requirements) .....	28

A.I.B.	Specification of a load under grid forming control (feed-in).....	29
A.I.C.	Definition of measurement variables .....	35
A.II.	Comparison of verification procedures.....	35
A.II.A.	Introduction .....	35
A.II.B.	Time curve up to the first peak and of the settled state.....	36
A.II.C.	Continuous envelope based on the simulation model with actual topology.....	38
A.II.D.	Comparison.....	44
A.III.	Examples of reference time varying curves with controlled voltage source .....	89